



FP KIT GW LoRa-ETH F868

KIT GW LoRa-ETH-F868, Kit comunicacions Gateway LoRa-Ethernet 868 MHz, estructura + base + ancoratges per a muntatge inclosos,

Code: E83G020021100 **DESCATALOGADO**

Especificacions

DC power supply

Installation category	CAT II 1500 V
Consumption	3.6 W
Nominal voltage	24 V \pm 10%

Environmental characteristics

Protection class	IP 00
Relative humidity (without condensation)	5 ... 95 %
Installation, location, position.	3000 m
Storage temperature	-25...+80 °C
Operating temperature	-25...+70 °C

Mechanical characteristics

Envelope	Self-extinguishing V0 plastic
Fastening	DIN rail
Weight (kg)	6,7

Current measurement circuit

Installation category	CAT II 1500 V
Impedance	0.5 m Ω
Minimum current measurement	0.1 A

Voltage measurement circuit

Installation category	CAT II 1500 V
Input impedance	2400 k Ω
Voltage measuring range	-25 ... -1500 Vdc
Maximum input voltage consumption	0,625 mA
Minimum measurement voltage (Vstart)	-25 V

Input

Accuracy	\pm 3°C
Range	-30 ... 100°C
Resolution	\pm 0,1 mA
Type	Pt100/1000

Standards

Electrical safety, Maximum height (m)	3000
Electrical safety, Installation category	Protection against electric shock: Double class II insulation



FP KIT GW LoRa-ETH F868

Code: E83G020021100

Standards

EN 61000-6-2, EN 61000-6-4, EN 61010-1, EN 61010-2-30

User interface

LED	4
-----	---

Digital inputs

Input/output insulation	Optoïllat
Quantity	3
Type	Contacte lliure de potencial
Maximum short-circuit current	3.2 mA
Maximum open circuit voltage	24 V

Kits GW-LoRa

Gateway Lora

CODI	TIPUS	Descripció
E83G020021100	FP KIT GW LoRa-ETH F868	Kit comunicacions Gateway LoRa-Ethernet 868 MHz, estructura + base + ancoratges per a muntatge inclosos
E83G020021200	FP KIT GW LoRa-ETH F915	Kit comunicacions Gateway LoRa-Ethernet 915 MHz, estructura + base + ancoratges per a muntatge inclosos
E83G020020100	GW LoRa-ETH-F868	Gateway LoRa-Ethernet 868 MHz
E83G020020200	FP GW LoRa-ETH	Gateway LoRa-Ethernet

The minimum configuration of the STM solution is made up of an STM-C module and an STM-S module